

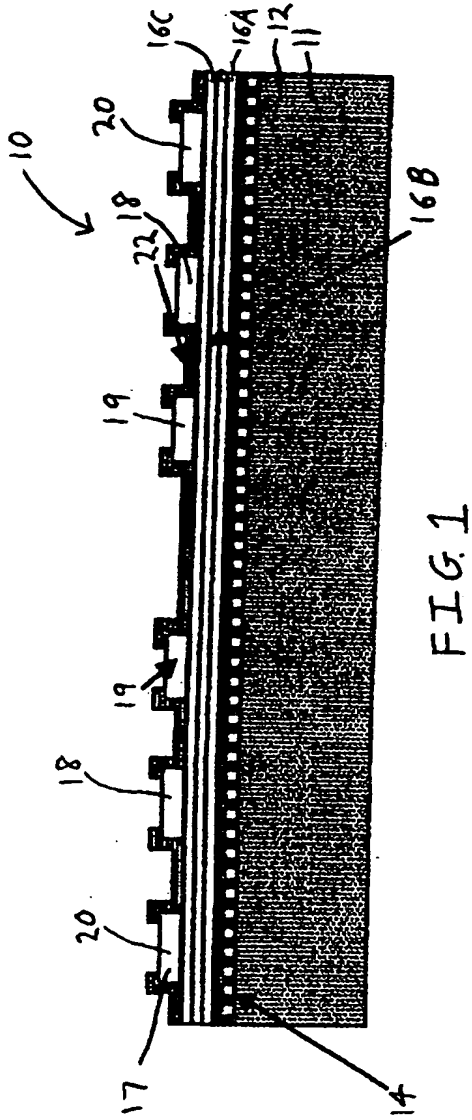
[illegible]

FIG. 1

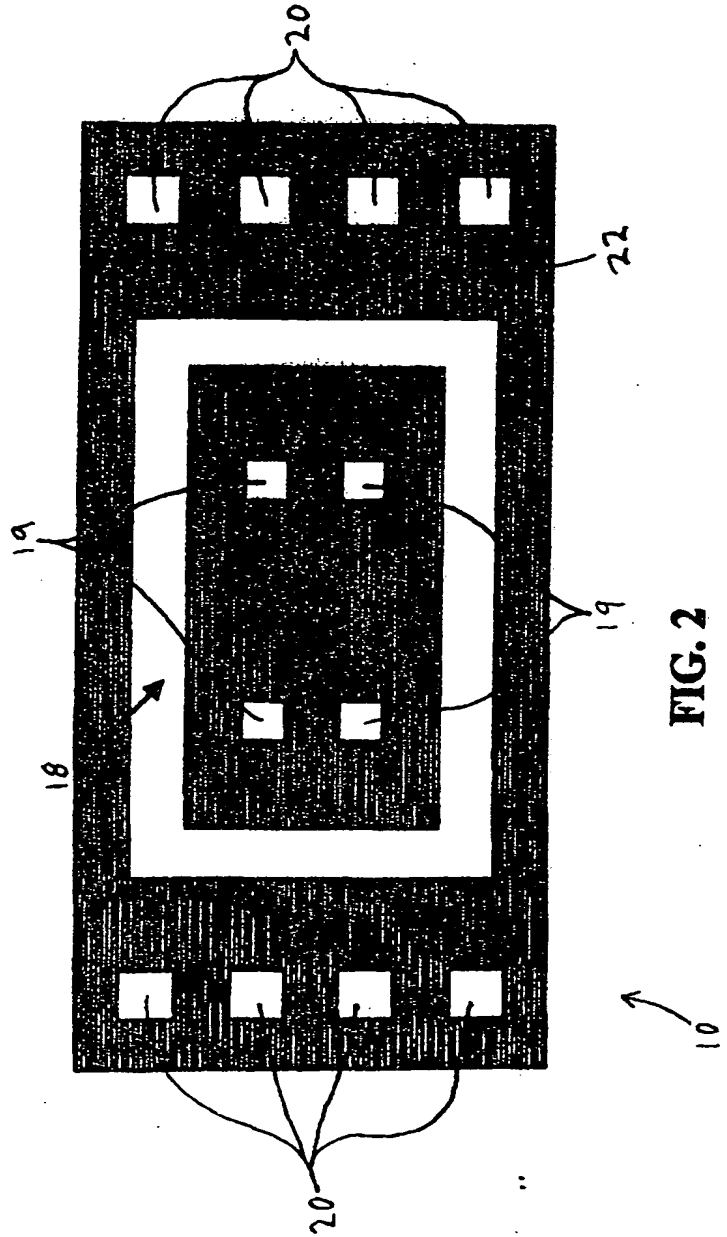


FIG. 2

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A cross-sectional view of a semiconductor device. A substrate 31 is covered by a layer 33. A wavy line 36 represents an interface. On top of this, there is a central gate structure 32 with a rectangular opening. To the left and right of the gate are rectangular regions 38 and 39. Further out are regions 37 and 30. The entire structure is on top of the substrate 31.



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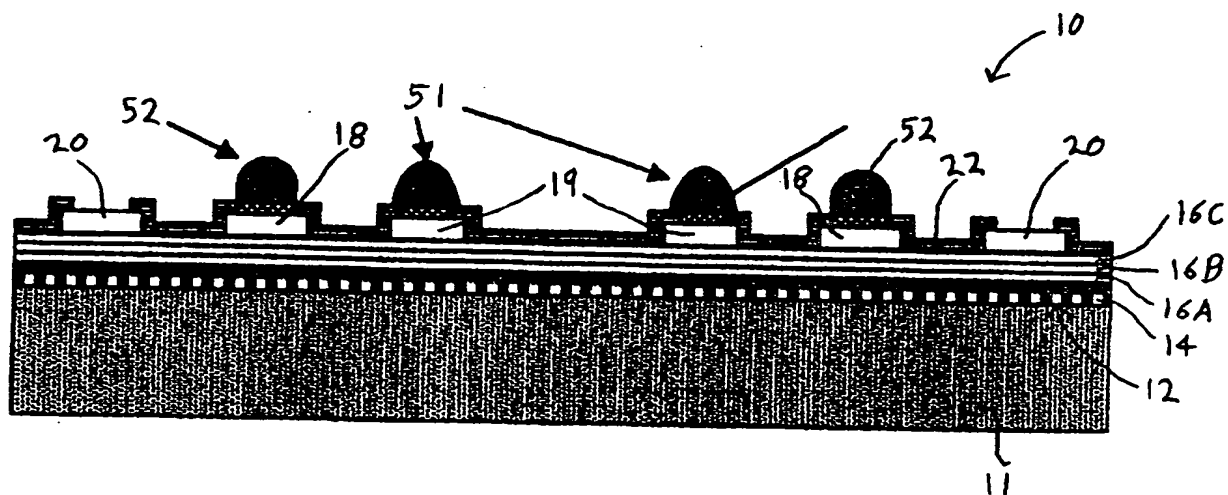


FIG. 5

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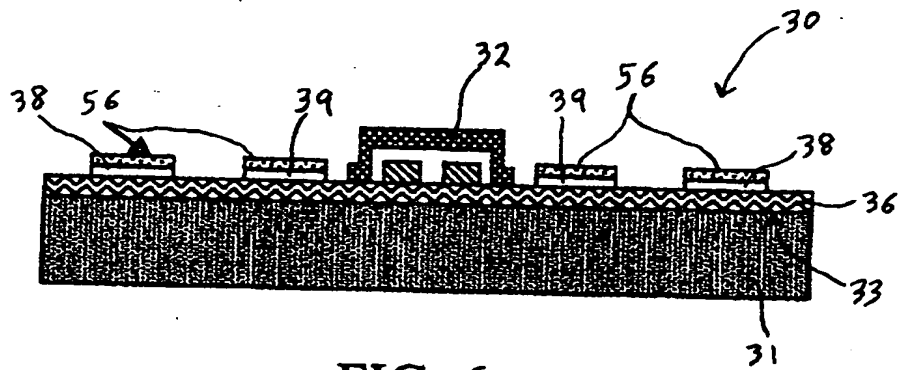


FIG. 6

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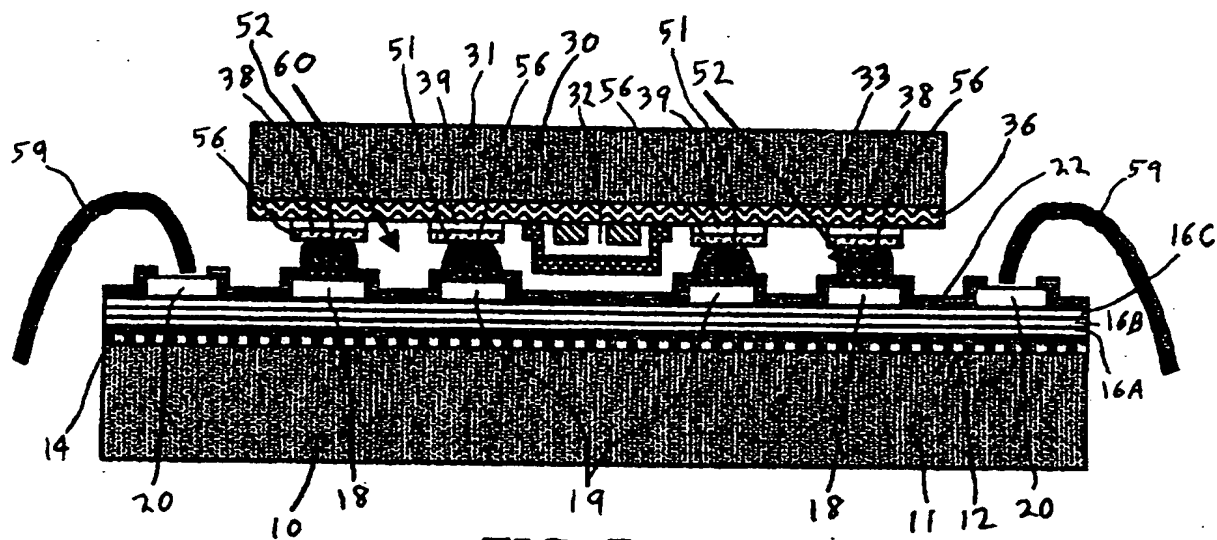


FIG. 7

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